If you are using a printed copy of this procedure, and not the on-screen version, then you <u>MUST</u> make sure the dates at the bottom of the printed copy and the on-screen version match.

The on-screen version of the Collider-Accelerator Department Procedure is the Official Version.

Hard copies of all signed, official, C-A Operating Procedures are kept on file in the C-A ESHQ

Training Office, Bldg. 911A.

C-A OPERATIONS PROCEDURES MANUAL

ATTACHMENT

4.120.13.b WXY (PEER 5) GATE TESTS

C-A-OPM Procedures in which this Attachment is used.					
4.120.13					

Hand Processed Changes

HPC No.	<u>Date</u>		Page Nos.	<u>Initials</u>	
	Approved:		gnature on File		
		Collider-Acc	celerator Depart	ment Chairman	Date

V. Castillo

4.120.13.b WXY (PEER 5) Gate Tests

PASS ANNUAL ACCEPTANCE TEST PROTOCOL

Division A Software Filename and Checksum: Title:	Checksum:
Division B Software Filename and Checksum: Title:	Checksum:
<u>Initial testing complete</u> :	
Test Team Leader's Name (Print):	Life Number:
	D. ()
Test Team Leader's Name (Sign):	Date://
Acceptance test procedure complete (following repairs and retesting if required):	
The definition of the desired Name (Desired).	I to Name have
Test Team Leader's Name (Print):	Life Number:
Test Team Leader's Name (Sign):	Date:/
Test results reviewed by:	
Safety Section Head's Name (Print):	Life Number:
Salety Section Head's Name (1 lint).	Life Number.
Safety Section Head's Name (Sign):	Date:/
Test results accepted by Radiation Safety Committee:	
100 10000 accepted by radiation barety Committee.	
RSC Member's Name (Print):	Life Number:
RSC Member's Name (Sign):	Date://

1.1 CONDUCT Visual check on Peer 5 gates following Table-1, below

	Micro	Switch	Elec	Gate		Gate Functions		Verify	Inspn	
Gate	Align	Opern	Wiring	Box	Lights	Open	Self- Closing	Latch	all x's Corr.	O.K. Init.
YGI1										
YGI2										
XGI1										
XGI2										
WGE1										
WGE2										
UED1										

Legend: $\sqrt{\ } = O.K.$ X = Problem N/A = Not Applicable

Table 1: Summary of Physical Inspection of Peer 5 Gates

1.2 Test of GATE at YGI1

	VERIFY PLACE	Gate at YGI1 has been inspected PEER 7 in Restricted Access (MODE 8)	
	VERIFY	PEER 7 is in Restricted Access	MODE 8
	PLACE	PEER 5 in Controlled Access (MODE 16)	
	VERIFY	PEER 5 is in Controlled Access	MODE 16
	VERIFY	The warning light on the inside of the gate: Call	
		MCR for Crossover Amber	ON
	VERIFY	Attempt to open YGI1 with Simultaneous Release	
		and S Key	FAIL
	VERIFY	Attempt to open YGI1 with Blue card	FAIL
	VERIFY	Attempt to open YGI1 with Expt.	FAIL
		card	
	VERIFY	During attempt with Expt. Card Reader light is	RED
	OPEN	Gate YGI1 with Simultaneous Release and #15 RC	
	OPEN	Gate YGI1 with Simultaneous Release and #15 RC Sweep Key	
	OPEN VERIFY		SOUNDS
		Sweep Key	SOUNDS OPEN
_	VERIFY	Sweep Key Simultaneous Release Buzzer Gate YGI1 is	
	VERIFY VERIFY	Sweep Key Simultaneous Release Buzzer	OPEN
	VERIFY VERIFY VERIFY	Sweep Key Simultaneous Release Buzzer Gate YGI1 is MCR sees the gate is The Electric Strike micro switch	OPEN OPEN
	VERIFY VERIFY VERIFY SECURE	Sweep Key Simultaneous Release Buzzer Gate YGI1 is MCR sees the gate is	OPEN OPEN MADE
	VERIFY VERIFY VERIFY SECURE HOLD	Sweep Key Simultaneous Release Buzzer Gate YGI1 is MCR sees the gate is The Electric Strike micro switch Both of the Peer 5 gate micro switches	OPEN OPEN MADE MADE
	VERIFY VERIFY VERIFY SECURE HOLD VERIFY	Sweep Key Simultaneous Release Buzzer Gate YGI1 is MCR sees the gate is The Electric Strike micro switch Both of the Peer 5 gate micro switches MCR sees the gate is	OPEN OPEN MADE MADE
	VERIFY VERIFY VERIFY SECURE HOLD VERIFY RELEASE	Sweep Key Simultaneous Release Buzzer Gate YGI1 is MCR sees the gate is The Electric Strike micro switch Both of the Peer 5 gate micro switches MCR sees the gate is P5 Div A micro switch MCR sees Div A	OPEN OPEN MADE MADE CLOSED
	VERIFY VERIFY VERIFY SECURE HOLD VERIFY RELEASE VERIFY	Sweep Key Simultaneous Release Buzzer Gate YGI1 is MCR sees the gate is The Electric Strike micro switch Both of the Peer 5 gate micro switches MCR sees the gate is P5 Div A micro switch	OPEN OPEN MADE MADE CLOSED OPEN
	VERIFY VERIFY VERIFY SECURE HOLD VERIFY RELEASE VERIFY HOLD	Sweep Key Simultaneous Release Buzzer Gate YGI1 is MCR sees the gate is The Electric Strike micro switch Both of the Peer 5 gate micro switches MCR sees the gate is P5 Div A micro switch MCR sees Div A Both of the Peer 5 gate micro switches	OPEN OPEN MADE MADE CLOSED OPEN MADE

	VERIFY HOLD	MCR sees Div B Both of the Peer 5 gate micro switches	OPEN MADE
	VERIFY RELEASE	MCR sees the gate is The Electric Strike micro switch	CLOSED
	VERIFY CLOSE	MCR sees the gate is The gate	OPEN
	VERIFY	MCR sees the gate is	CLOSED
	VERIFY	The YGI1 gate box Gate Reset light is	OFF
	RESET	The gate with #13 Inj Sweep key at YGI1 gate box	
	VERIFY	MCR sees the gate is	RESET
	VERIFY OPEN	The YGI1 gate box Gate Reset light is The gate	ON
	VERIFY	MCR sees the gate is	OPEN
	VERIFY	The YGI1 gate box Gate Reset light is	OFF
	CLOSE	The gate	
	PLACE	PEER 5 in Restricted Access (MODE 8)	
	VERIFY	PEER 5 is in Restricted Access	MODE 8
	VERIFY	PEER 7 is in Restricted Access	MODE 8
	VERIFY VERIFY	Attempt to open YGI1 with Blue card Attempt to open YGI1 with Expt	SUCCESSFUL FAIL
	VERIF I	card	FAIL
	VERIFY	Attempt to open YGI1 with S key	SUCCESSFUL
	VERIFY	Attempt to open YGI1 with #15 RC Sweep key	SUCCESSFUL
	PLACE	PEER 5 in MODE 2	
	VERIFY	PEER 5 is in Safe Access	MODE 2
	VERIFY	PEER 7 is in Restricted Access	MODE 8
	VERIFY	Attempt to open YGI1 with Blue card	FAIL
	VERIFY	Attempt to open YGI1 with Expt	FAIL
	VERIFY	Attempt to open YGI1 with S key and SR	FAIL
	VERIFY	Attempt to open YGI1 with #15 RC Sweep key	FAIL
	VERIFY	Attempt to open YGI1 with #15 RC Sweep key and	
_		SR	SUCCESSFUL
	CLOSE	Gate YGI1	

CHECK for acceptance of Test of GATE at YGI1

1.3 Test of GATE at YGI2

	VERIFY	Gate at YGI2 has been inspected	
	PLACE	PEER 7 in Restricted Access (MODE 8)	
	VERIFY	PEER 7 is in Restricted Access	MODE 8
_	PLACE	PEER 5 in Controlled Access (MODE 16)	MODE 16
	VERIFY	PEER 5 is in Controlled Access	MODE 16
	VERIFY	The warning light on the inside of the gate: Call MCR for Crossover Amber	OFF
	VERIFY	Attempt to open YGI2 with Simultaneous Release	OFF
Ц	VEXIF 1	and S Key	FAIL
	VERIFY	Attempt to open YGI2 with Blue card	FAIL
	VERIFY	Attempt to open YGI2 with Expt.	FAIL
_		card	
	VERIFY	During attempt with Expt. Card Reader light is	RED
	OPEN	Gate YGI2 with Simultaneous Release and #13 Inj	
	VERIFY	Sweep Key Simultaneous Release Buzzer	SOUNDS
	VERIFY	Gate YGI2 is	OPEN
	VERIFY	MCR sees the gate is	OPEN
Ц	SECURE	The Electric Strike micro switch	MADE
	HOLD	Both of the Peer 5 gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
_	RELEASE	P5 Div A micro switch	
	VERIFY	MCR sees Div A	OPEN
	HOLD	Both of the P5 gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
	RELEASE	P5 Div B micro switch	
	VERIFY	MCR sees Div B	OPEN
	HOLD	Both of the P5 gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
_	RELEASE	The Electric Strike micro switch	ODEN
	VERIFY	MCR sees the gate is	OPEN
	CLOSE VERIFY	The gate MCP gate the gate is	CLOSED
	VERIFY	MCR sees the gate is The YGI2 gate box Gate Reset light is	OFF
	RESET	The gate with #13 Inj Sweep key at YGI2 gate box	OFF
П	VERIFY	MCR sees the gate is	RESET
	VERIFY	The YGI2 gate box Gate Reset light is	ON
	OPEN	The gate	011
	VERIFY	MCR sees the gate is	OPEN
	VERIFY	The YGI2 gate box Gate Reset light is	OFF
	CLOSE	The gate	
	PLACE	PEER 5 in Restricted Access (MODE 8)	
	VERIFY	PEER 5 is in Restricted Access	MODE 8
	VERIFY	PEER 7 is in Restricted Access	MODE 8
	VERIFY	Attempt to open YGI2 with Blue card	SUCCESSFUL
	VERIFY	Attempt to open YGI2 with Expt	FAIL
_	*/******	card	OTTO CERCICE
	VERIFY	Attempt to open YGI2 with S key	SUCCESSFUL
	VERIFY	Attempt to open YGI2 with #13 Inj Sweep key	SUCCESSFUL
	PLACE	PEER 5 in MODE 2	

VERIFY	PEER 5 is in Safe Access	MODE 2
VERIFY	PEER 7 is in Restricted Access	MODE 8
VERIFY	Attempt to open YGI2 with Blue card	FAIL
VERIFY	Attempt to open YGI2 with Expt	
	card	FAIL
VERIFY	Attempt to open YGI2 with S key and SR	FAIL
VERIFY	Attempt to open YGI2 with #13 Inj Sweep key	FAIL
VERIFY	Attempt to open YGI2 with #13 Inj Sweep key and	
	SR	SUCCESSFUL
CLOSE	Gate YGI2	

☐ CHECK for acceptance of Test of GATE at YGI2

1.4 Test of GATE at XGI1

	VERIFY	Gate at XGI1 has been inspected	
	PLACE	PEER 7 in Restricted Access (MODE 8)	
	VERIFY	PEER 7 is in Restricted Access	MODE 8
	PLACE	PEER 5 in Controlled Access (MODE 16)	
	VERIFY	PEER 5 is in Controlled Access	MODE 16
	VERIFY	The warning light on the inside of the gate: Call	
		MCR for Crossover Amber	ON
	VERIFY	Attempt to open XGI1 with Simultaneous Release	
		and S Key	FAIL
	VERIFY	Attempt to open XGI1 with Blue card	FAIL
	VERIFY	Attempt to open XGI1 with Expt.	FAIL
	*********	card	DED
	VERIFY	During attempt with Expt. Card Reader light is	RED
	OPEN	Gate XGI1 with Simultaneous Release and #15 R	
	<u> </u>	C SweepKey	
	VERIFY	Simultaneous Release Buzzer	SOUNDS
	VERIFY	Gate XGI1 is	OPEN
	VERIFY	MCR sees the gate is	OPEN
_	SECURE	The Electric Strike micro switch	MADE
	HOLD	Both of the Peer 5 gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
	RELEASE	P5 Div A micro switch	
	VERIFY	MCR sees Div A	OPEN
	HOLD	Both of the Peer 5 gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
	RELEASE	P5 Div B micro switch	
	VERIFY	MCR sees Div B	OPEN
	HOLD	Both of the Peer 5 gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
	RELEASE	The Electric Strike micro switch	
	VERIFY	MCR sees the gate is	OPEN
	CLOSE	The gate	
	VERIFY	MCR sees the gate is	CLOSED
	VERIFY	The XGI1 gate box Gate Reset light is	OFF
	RESET	The gate with #13 Inj Sweep key at XGI1 gate box	
	VERIFY	MCR sees the gate is	RESET

VERIFY	The XGI1 gate box Gate Reset light is	ON
OPEN	The gate	
VERIFY	MCR sees the gate is	OPEN
VERIFY	The XGI1 gate box Gate Reset light is	OFF
CLOSE	The gate	
PLACE	PEER 5 in Restricted Access (MODE 8)	
VERIFY	PEER 5 is in Restricted Access	MODE 8
VERIFY	PEER 7 is in Restricted Access	MODE 8
VERIFY	Attempt to open XGI1 with Blue card	SUCCESSFUL
VERIFY	Attempt to open XGI1 with Expt.	FAIL
	card	
VERIFY	Attempt to open XGI1 with S key	SUCCESSFUL
VERIFY	Attempt to open XGI1 with #15 RC Sweep key	SUCCESSFUL
PLACE	PEER 5 in MODE 2	
VERIFY	PEER 5 is in Safe Access	MODE 2
VERIFY	PEER 7 is in Restricted Access	MODE 8
VERIFY	Attempt to open XGI1 with Blue card	FAIL
VERIFY	Attempt to open XGI1 with Expt.	
	card	FAIL
VERIFY	Attempt to open XGI1 with S key and SR	FAIL
VERIFY	Attempt to open XGI1 with #15 RC Sweep key	FAIL
VERIFY	Attempt to open XGI1 with #15 RC Sweep key and	
	SR	SUCCESSFUL
CLOSE	Gate XGI1	

1.5 Test of GATE at XGI2

	VERIFY PLACE	Gate at XGI2 has been inspected PEER 7 in Restricted Access (MODE 8)	
	VERIFY PLACE	PEER 7 is in Restricted Access PEER 5 in Controlled Access (MODE 16)	MODE 8
	VERIFY VERIFY	PEER 5 is in Controlled Access The warning light on the inside of the gate: Call	MODE 16
	VERIFY	MCR for Crossover Amber Attempt to open XGI2 with Simultaneous Release	OFF
		and S Key	FAIL
	VERIFY	Attempt to open XGI2 with Blue card	FAIL
	VERIFY	Attempt to open XGI2 with Expt.	FAIL
	VERIFY	card During attempt with Expt. Card Reader light is	RED
	OPEN	Gate XGI2 with Simultaneous Release and #13 Inj Sweep Key	
	VERIFY	Simultaneous Release Buzzer	SOUNDS
	VERIFY	Gate XGI2 is	OPEN
	VERIFY	MCR sees the gate is	OPEN
	SECURE	The Electric Strike micro switch	MADE
	HOLD	Both of the Peer 5 gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
_	RELEASE	P5 Div A micro switch	ODEN
	VERIFY HOLD	MCR sees Div A Both of the P5 gate micro switches	OPEN MADE
	VERIFY	MCR sees the gate is	CLOSED
П	RELEASE	P5 Div B micro switch	CLOSED
	VERIFY	MCR sees Div B	OPEN
	HOLD	Both of the P5 gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
	RELEASE	The Electric Strike micro switch	
	VERIFY	MCR sees the gate is	OPEN
_	CLOSE	The gate	CI OCED
	VERIFY	MCR sees the gate is	CLOSED
	VERIFY	The XGI2 gate box Gate Reset light is	OFF
П	RESET VERIFY	The gate with #13 Inj Sweep key at XGI2 gate box MCR sees the gate is	RESET
	VERIFY	The XGI2 gate box Gate Reset light is	ON
П	OPEN	The gate	OIV
	VERIFY	MCR sees the gate is	OPEN
	VERIFY	The XGI2 gate box Gate Reset light is	OFF
	CLOSE	The gate	
	PLACE	PEER 5 in Restricted Access (MODE 8)	
	VERIFY	PEER 5 is in Restricted Access	MODE 8
	VERIFY	PEER 7 is in Restricted Access	MODE 8
	VERIFY	Attempt to open XGI2 with Blue card	SUCCESSFUL
	VERIFY	Attempt to open XGI2 with Expt	FAIL
	VERIFY	Attempt to open XGI2 with S key	SUCCESSFUL
	VERIFY	Attempt to open XGI2 with #13 Inj Sweep key	SUCCESSFUL
	PLACE	PEER 5 in MODE 2	

VERIFY	PEER 5 is in Safe Access	MODE 2
VERIFY	PEER 7 is in Restricted Access	MODE 8
VERIFY	Attempt to open XGI2 with Blue card	FAIL
VERIFY	Attempt to open XGI2 with Expt.	
	card	FAIL
VERIFY	Attempt to open XGI2 with S key and SR	FAIL
VERIFY	Attempt to open XGI2 with #13 Inj Sweep key	FAIL
VERIFY	Attempt to open XGI2 with #13 Inj Sweep key and	
	SR	SUCCESSFUL
CLOSE	Gate XGI2	

☐ CHECK for acceptance of Test of GATE at XGI2

1.6 Test of ENTRY GATE at WGE1

	VERIFY PLACE	ENTRY Gate at WGE1 has been inspected PEER 5 in Controlled Access	
	VERIFY	PEER 5 is in Controlled Access	MODE 16
	VERIFY	The warning light on the inside of the gate indicates:	11102210
		CALL MCR FOR EXIT AMBER	ON
	VERIFY	The Exterior gate box Controlled Access light is	ON
	VERIFY	Attempt to open WGE1 with Simultaneous Release	
		and S Key	FAIL
	VERIFY	Attempt to open WGE1 with Blue card	FAIL
	VERIFY	Attempt to open WGE1 with Expt.	FAIL
		card	
	VERIFY	During attempt with Expt. Card Reader light is	RED
	OPEN	Gate WGE1 with Simultaneous Release and #12	
_	T/EDIET/	Inj CA Key	COLINIDA
	VERIFY	Simultaneous Release Buzzer	SOUNDS
	VERIFY	Gate WGE1 is	OPEN
	VERIFY	MCR sees the gate is	OPEN
	SECURE	The Electric Strike micro switch	MADE
	HOLD	Both of the gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
	RELEASE	Div A micro switch	
	VERIFY	MCR sees Div A	OPEN
	HOLD	Both of the gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
	RELEASE	Div B micro switch	
	VERIFY	MCR sees Div B	OPEN
	HOLD	Both of the gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
	RELEASE	The Electric Strike micro switch	
	VERIFY	MCR sees the gate is	OPEN
	CLOSE	The gate	
	VERIFY	MCR sees the gate is	CLOSED
	RESET	The gate with Remote reset from MCR	
	VERIFY	MCR sees the gate is	RESET

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	OPEN VERIFY CLOSE	The gate MCR sees the gate is The gate	OPEN			
PLACE VERIFY VERIFY VERIFY		PEER 5 in Restricted Access PEER 5 is in Restricted Access The Exterior gate box Restricted Access light is The warning lights on both sides of the gate indicate:	MODE 8 ON			
	VERIFY VERIFY VERIFY	CALL MCR FOR EXIT AMBER Attempt to open gate WGE1 with S key is Attempt to open gate WGE1 with #12 Inj CA key is Attempt to open gate WGE1 with Blue Card is	OFF SUCCESSFUL SUCCESSFUL SUCCESSFUL			
	CLOSE VERIFY	Gate WGE1 Attempt to open WGE1 with Expt card	FAIL			
	VERIFY	During attempt with Expt. Card Reader light is	RED			
	PLACE VERIFY VERIFY VERIFY	PEER 5 in Safe Access (Mode 2) PEER 5 is in Safe Access The Exterior gate box Controlled Access light is The warning light on inside of the gate indicates: CALL MCR FOR EXIT AMBER Gate WGE1 with Simultaneous Release and S Key	MODE 2 ON			
	VERIFY	Attempt to open gate WGE1 with Simultaneous Release and S Key	FAIL			
	OPEN VERIFY CLOSE	Gate WGE1 with Simultaneous Release and #12 Inj CA Key Gate WGE1 is Gate WGE1	OPEN			
	CHECK fo	or test acceptance of ENTRY GATE at WGE1				
1.7 Test of ENTRY GATE at WGE2 UERIFY ENTRY Gate at WGE2 has been inspected PLACE PEER 5 in Controlled Access						
	VERIFY VERIFY	PEER 5 is in Controlled Access The warning light on the inside of the gate indicates: CALL MCD FOR EXIT.	MODE 16			
	VERIFY VERIFY	CALL MCR FOR EXIT AMBER The Exterior gate box Controlled Access light is Attempt to open WGE2 with Simultaneous Release and S Key	ON ON			
	VERIFY VERIFY	Attempt to open WGE2 with Blue card Attempt to open WGE2 with Expt card	FAIL FAIL FAIL			
	VERIFY	During attempt with Expt. Card Reader light is	RED			
	OPEN	Gate WGE2 with Simultaneous Release and #12 Inj CA Key				
	VERIFY	Simultaneous Release Buzzer	SOUNDS			
	VERIFY	Gate WGE2 is	OPEN			

	VERIFY SECURE	MCR sees the gate is The Electric Strike micro switch	OPEN MADE
	HOLD	Both of the gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
	RELEASE	Div A micro switch MCR sees Div A	OPEN
	VERIFY HOLD	Both of the gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
	RELEASE	Div B micro switch	
	VERIFY	MCR sees Div B	OPEN
	HOLD	Both of the gate micro switches	MADE
	VERIFY	MCR sees the gate is	CLOSED
	RELEASE	The Electric Strike micro switch	ODEN
	VERIFY CLOSE	MCR sees the gate is The gate	OPEN
	VERIFY	MCR sees the gate is	CLOSED
	RESET	The gate with Remote reset from MCR	
	VERIFY	MCR sees the gate is	RESET
	OPEN	The gate	
	VERIFY	MCR sees the gate is	OPEN
	CLOSE	The gate	
	PLACE	PEER 5 in Restricted Access	
	VERIFY	PEER 5 is in Restricted Access	MODE 8
	VERIFY	The Exterior gate box Restricted Access light is	ON
	VERIFY	The warning lights on both sides of the gate indicate:	
		CALL MCR FOR EXIT AMBER	OFF
	VERIFY	Attempt to open gate WGE2 with S key is	SUCCESSFUL
	VERIFY	Attempt to open gate WGE2 with #12 Inj CA key is	SUCCESSFUL
	VERIFY CLOSE	Attempt to open gate WGE2 with Blue Card is Gate WGE2	SUCCESSFUL
	VERIFY	Attempt to open WGE2 with Expt	FAIL
	,	card	
	VERIFY	During attempt with Expt. Card Reader light is	RED
	PLACE	PEER 5 in Safe Access (Mode 2)	
	VERIFY	PEER 5 is in Safe Access	MODE 2
	VERIFY	The Exterior gate box Controlled Access light is	ON
	VERIFY	The warning light on inside of the gate indicates:	
	ODEN	CALL MCR FOR EXIT AMBER	ON
	OPEN VERIFY	Gate WGE2 with Simultaneous Release and S Key Attempt to open gate WGE2 with Simultaneous	
Ц	VEXII I	Release and S Key	FAIL
	OPEN	Gate WGE2 with Simultaneous Release and #12	
		Inj CA Key	
	VERIFY	Gate WGE2 is	OPEN
	CLOSE	Gate WGE2	

1.8	Test of	f Gate UEDI	Aux		
		VERIFY	Gate has been inspected		
		VERIFY	The door cannot be opened from the out		
		PLACE	PEER 5 in Controlled Access (MODE	16)	
		VERIFY	PEER 5 is in Controlled Access	MODE 1	16
		OPEN	The door		
		VERIFY	MCR sees Div $\mathbf{A} \square$ and Div $\mathbf{B} \square$	OPEN	
		HOLD	Both of the Peer 5 micro switches	MADE	
		VERIFY	MCR sees the door is	CLOSEI	D
		RELEASE	P5 Div A door micro switch	OPEN	
		VERIFY	MCR sees Div A	OPEN	
	_	HOLD	Both of the P5 door micro switches MCR sees the door is	MADE	.
		VERIFY		CLOSEI	ט
		RELEASE VERIFY	P5 Div B door micro switch MCR sees Div B	OPEN	
		CLOSE	Gate UED1	OFEN	
		VERIFY	MCR sees Div A □ and Div B □	CLOSEI	n
	Ш	VERIF I	MCR sees DIV A and DIV B	CLOSEI	
		PLACE	PEER 5 in Restricted Access		
		VERIFY	PEER 5 is in Restricted Access	MODE 8	3
		OPEN	The door		
		VERIFY	MCR sees Div A \square and Div B \square	OPEN	
		CLOSE	Gate UED1		
		VERIFY	MCR sees Div A \square and Div B \square	CLOSEI	D
		PLACE	PEER 5 in Safe Access		
		VERIFY	PEER 5 is in Safe Access	MODE 2	2
		OPEN	The door		
		VERIFY	MCR sees Div A □ and Div B □	OPEN	
	Ц	CLOSE	Gate UED1	OI EIV	
		VERIFY	MCR sees Div A □ and Div B □	CLOSEI	D
		, 23111 I	MCK sees DIV II and DIV D	02002	
		CHECK for	acceptance of Test of Gate UED1 Aux		
			END OF TEST PROCEDURE		
	_				
TTL: Sign for o	comple	tion of initial (testing:		
				Doto: / /	
				Date://	
TTL: Sign for	comple	etion of final to	esting:		
				Date://	